

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10815453
Filing Date	2004-03-31
First Named Inventor	Ricardo Gonzalez
Art Unit	2157
Examiner Name	Marshall McLeod
Attorney Docket Number	PA2718US

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	4783738		1988-11-08	Li	
	2	5055997		1991-10-08	Sluijter	
	3	5247689		1993-09-21	Ewert	
	4	5274782		1993-12-28	Chalasani	
	5	5299317		1994-03-29	Chen	
	6	5471628		1995-11-28	Phillips	
	7	5684980		1997-11-04	Casselman	
	8	5850564		1998-12-15	Ting	

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10815453
Filing Date	2004-03-31
First Named Inventor	Ricardo Gonzalez
Art Unit	2157
Examiner Name	Marshall McLeod
Attorney Docket Number	PA2718US

9	6026478		2000-02-15	Dowling	
10	6167502		2000-12-26	Pechanek	
11	6343337		2002-01-29	Dubey	
12	6374403		2002-04-16	Darte	
13	6467009		2002-10-15	Winegarden	
14	6557092		2003-04-29	Callen	
15	6721866		2004-04-13	Roussel	
16	6831690		2004-12-14	John	
17	6883084		2005-04-19	Donohoe	
18	6986127		2006-01-10	Newlin	
19	6996709		2006-02-07	Arnold	

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)

Application Number	10815453
Filing Date	2004-03-31
First Named Inventor	Ricardo Gonzalez
Art Unit	2157
Examiner Name	Marshall McLeod
Attorney Docket Number	PA2718US

20	7086047		2006-08-01	Edwards	
21	7269616		2007-09-11	Rupp	
22	7350054		2008-03-25	Furuta	
23	7373642		2008-05-13	Williams	
24	7412684		2008-08-12	Gutberlet	

If you wish to add additional U.S. Patent citation information please click the Add button.

U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20040193852		2004-09-30	Johnson	

If you wish to add additional U.S. Published Application citation information please click the Add button.

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ² i	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
	1							<input type="checkbox"/>

If you wish to add additional Foreign Patent Document citation information please click the Add button

NON-PATENT LITERATURE DOCUMENTS

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10815453
Filing Date	2004-03-31
First Named Inventor	Ricardo Gonzalez
Art Unit	2157
Examiner Name	Marshall McLeod
Attorney Docket Number	PA2718US

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	RUPP et al.; U.S. Patent Application No. 10/746018 entitled "Architecture and Method for Reconfigurable Data Path Processing" filed Dec. 23 2003.	<input type="checkbox"/>
	2	BARAT FRANCISCO et al. "Reconfigurable Instruction Set Processors: A Survey" IEEE 2000 0-7695-0688-2/00.	<input type="checkbox"/>
	3	BORKAR SHEKHAR et al. "iWarp: An Integrated Solution to High-Speed Parallel Computing" IEEE 1998 pp 330-339.	<input type="checkbox"/>
	4	CARRILLO et al.; "The Effect of Reconfigurable Units in Superscalar Processors" 2001; ACM.	<input type="checkbox"/>
	5	DINIZ et al. "Automatic Synthesis of Data Storage and Control Structures for FPGA-based Computing Engines" 2000 IEEE pp 91-100.	<input type="checkbox"/>
	6	GONZALEZ "Xtensa: A Configurable and Extensible Processor" March-April 2000 IEEE Micro pp 60-70.	<input type="checkbox"/>
	7	JACOB et al; " Memory Interfacing and Instruction Specification ofr Reconfigurable Processors" 1999; ACM	<input type="checkbox"/>
	8	TAYLOR MICHAEL BEDFORD et al. "The Raw Microprocessor: A Computational Fabric for Software Circuits and General-Purpose Programs" Microarchitecture IEEE Micro March-April 2002 pp 25-35.	<input type="checkbox"/>
	9	WAINGOLD ELLIOT et al. "Baring It All to Software: Raw Machines" Computer September 1997 IEEE pp 86-93.	<input type="checkbox"/>
	10	http://www.tensilica.com/products/xtensa_overview.htm Xtensa Processor Overview	<input type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10815453
Filing Date	2004-03-31
First Named Inventor	Ricardo Gonzalez
Art Unit	2157
Examiner Name	Marshall McLeod
Attorney Docket Number	PA2718US

If you wish to add additional non-patent literature document citation information please click the Add button

EXAMINER SIGNATURE

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ See Kind Codes of USPTO Patent Documents at www.USPTO.GOV or MPEP 901.04. ² Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). ³ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁴ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language translation is attached.